



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Francesco Pappalardo et al.

Application No.

10/757,772

Filed

: January 14, 2004

For

METHOD AND DEVICE FOR TRANSMITTING DATA ON A

SINGLE LINE, IN PARTICULAR FOR TRANSMITTING DATA ON A BUS WITH MINIMIZATION OF THE BUS SWITCHING ACTIVITY, AND CORRESPONDING COMPUTER PRODUCT

Art Unit

2661

Docket No.

854063.740

Date

June 21, 2004

Mail Stop Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Information Disclosure Statement has been timely filed, however, the Commissioner is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC

David V. Carlson

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Enclosures:

Form PTO-1449 Cited References (6)

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Sheet 1 of 1

Date: June 21, 2004

FORM PTO-1449 (REV.7-80)

JUN 2 1 2004 INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

APPLICATION NO. ATTY. DOCKET NO. 854063.740 10/757,772

APPLICANTS

Francesco Pappalardo et al.

FILING DATE January 14, 2004 **GROUP ART UNIT** 2661

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	4,667,337	05/19/87	Fletcher	377	41	
	AB	2002/0156953	10/24/02	Beiley et al.	710	105	
-	AC						
•	AD						
	AE						
•	AF						
	AG						,
	АН						
	ΑI						
•	AJ						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
	DOCUMENT NUMBER DATE COUNTRY		YES	NO	
AK	9-69075	11/03/97	JP (+ Abstract in English)		-
AL	0 886 220 A2	12/23/98	EP		
AM	·				
AN					
ΑÖ					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AP	Pappalardo, F. et al., "Bus Switching Architecture," STMicroelectronics.
AQ	Abstract of European Patent Application No. 03425013.4 filed January 14, 2003.
 AR	

EXAMINER

DATE CONSIDERED

* EXAMINER:

Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).